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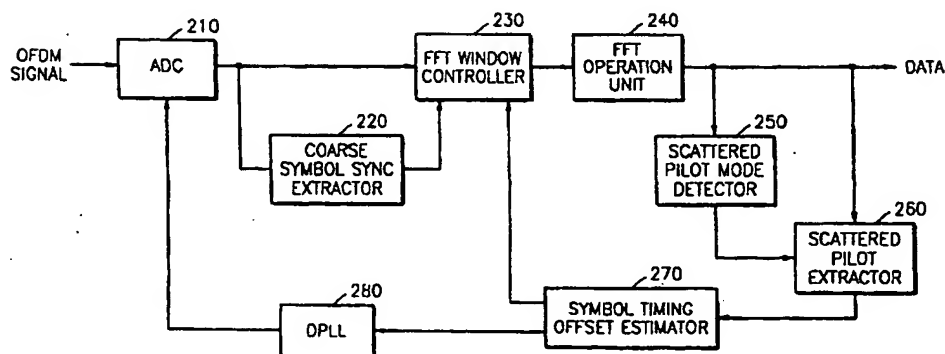
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(54) OFDM receiving system for performing system timing synchronization using guard interval and method for the same

(57) An orthogonal frequency division multiplex (OFDM) receiving system for changing a fast Fourier transform (FFT) window period using a guard interval, and a method for the same are provided. The method includes extracting scattered pilots, which are inserted into a symbol at intervals of predetermined samples, from a received OFDM signal, estimating a symbol timing offset using the extracted scattered pilots and setting a symbol timing synchronization point in the data transmission interval, and adding samples in a predeter-

mined interval in the data transmission interval to samples in an interval obtained by subtracting a predetermined length from the useful symbol interval starting from the symbol timing synchronization point and performing FFT with respect to the result of addition. Accordingly, symbol timing synchronization and channel equalization can be accurately performed without having ISI even when a symbol start point is not correctly decided under a multi-channel (or a Rayleigh channel) or a pre-ghost channel environment.

FIG. 2



## Description

[0001] The present invention relates to an orthogonal frequency division multiplex (OFDM) receiving system, and more particularly, to an OFDM receiving system for changing a fast Fourier transform (FFT) window period using a guard interval, and a method for the same.

[0002] Generally, OFDM transmitting systems transmit information over a subcarrier using fast Fourier transform (FFT) and add a guard interval to the front portion of a useful symbol in order to reduce the influence of a multi-path. OFDM receiving systems find the boundary between a guard interval and a useful symbol interval in a received OFDM signal and perform FFT window timing synchronization in order to perform FFT with respect to only a useful symbol. For this, OFDM receiving systems roughly find an FFT interval using the similarity between the guard interval and the useful symbol.

[0003] FIG. 1 is a conceptual diagram of symbol timing synchronization in a conventional OFDM receiving system. Referring to (a) of FIG. 1, a single symbol is composed of a guard interval  $T_g$  and a useful symbol interval  $T_u$ . A window timing controller (not shown) controlling window timing determines the start point of a symbol using a guard interval and inputs  $N$  samples starting from the determined symbol start point to an FFT operation unit (not shown). Here, the window timing controller inputs samples corresponding to the useful symbol interval  $T_u$  starting from a symbol timing synchronization point of the FFT window period to the FFT operation unit.

[0004] However, when the FFT window period is set, as shown in (a) of FIG. 1, the window timing controller does not determine a correct symbol start point under a pre-ghost channel environment having a multi-path resulting in intersymbol interference (ISI) occurring between a back portion of a symbol and a portion of the next symbol, as shown in (b) of FIG. 1. Such ISI causes the performance of the OFDM receiving system to be degraded.

[0005] To solve the above problems, it is the object of the present invention to provide an orthogonal frequency division multiplex (OFDM) receiving method for preventing intersymbol interference occurring in a multi-channel or a pre-ghost channel by changing a fast Fourier transform (FFT) window period using a guard interval transmitted before a useful symbol.

[0006] According to an aspect of the present invention, there is provided an OFDM receiving system for performing the OFDM receiving method.

[0007] To achieve the object of the invention, there is provided an OFDM receiving method of performing FFT with respect to a data transmission interval of a symbol unit composed of a guard interval and a useful symbol interval. The method includes extracting scattered pilots, which are inserted into a symbol at intervals of predetermined samples, from a received OFDM signal, estimating a symbol timing offset using the extracted scat-

tered pilots and setting a symbol timing synchronization point in the data transmission interval, and adding samples in a predetermined interval in the data transmission interval to samples in an interval obtained by subtracting a predetermined length from the useful symbol interval starting from the symbol timing synchronization point and performing FFT with respect to the result of addition.

[0008] According to another aspect of the invention, there is provided an OFDM receiving system for performing FFT with respect to a data transmission interval of a symbol unit composed of a guard interval and a useful symbol interval. The system includes an analog-to-digital converter for converting an OFDM signal into digital complex samples, an FFT operation unit for performing FFT with respect to the useful symbol interval, a scattered pilot extractor for extracting scattered pilots from samples output from the FFT operating unit, a symbol timing offset estimator for estimating a symbol timing offset using scattered pilots extracted by the scattered pilot extractor, and an FFT window controller for setting a symbol timing synchronization point in a data transmission interval received from the analog-to-digital converter, adding samples in a predetermined interval extending from the symbol timing synchronization point toward the front of the data transmission interval to samples in an interval obtained by subtracting a predetermined length from a useful symbol interval starting from the symbol timing synchronization point, and inputting the result of addition to the FFT operation unit.

[0009] The above object and advantages of the present invention will become more apparent by describing in detail preferred embodiments thereof with reference to the attached drawings in which:

FIG. 1 is a conceptual diagram of symbol timing synchronization in a conventional orthogonal frequency division multiplex (OFDM) receiving system;

FIG. 2 is a block diagram of an entire OFDM receiving system performing symbol timing synchronization according to the present invention;

FIG. 3 is a conceptual diagram of a method of setting a fast Fourier transform (FFT) window period in an FFT window controller according to the present invention;

FIG. 4 is a conceptual diagram of a method of resetting an FFT window period in an FFT window controller and inputting reset information to an FFT operation unit;

FIG. 5 is a flowchart of a symbol timing synchronization method in an OFDM receiving system according to the present invention;

FIG. 6 is a graph of the performance of the present invention versus the performance of prior art in symbol timing synchronization; and

FIG. 7 is a graph of symbol error rates (SERs) exhibited after symbol timing synchronization and channel equalization according to the present in-

vention and prior art.

[0010] Hereinafter, embodiments of the present invention will be described in detail with reference to the attached drawings.

[0011] FIG. 2 is a block diagram of an entire orthogonal frequency division multiplex (OFDM) receiving system performing symbol timing synchronization according to the present invention. The OFDM receiving system of FIG. 2 includes an analog-to-digital converter (ADC) 210, a coarse symbol sync extractor 220, a fast Fourier transform (FFT) window controller 230, an FFT operation unit 240, a scattered pilot mode detector 250, a scattered pilot extractor 260, a symbol timing offset estimator 270, and a digital phase locked loop (DPLL) 280.

[0012] The ADC 210 converts an OFDM signal into digital complex samples having a sample rate of 9.14 MHz.

[0013] The coarse symbol sync extractor 220 extracts a coarse symbol start point from received digital complex samples.

[0014] The FFT window controller 230 detects a coarse symbol start point using a coarse symbol sync signal extracted by the coarse symbol sync extractor 220, sets a symbol timing synchronization point using an offset value estimated by a symbol timing offset estimator 270, adds samples in a predetermined interval extending from the symbol timing synchronization point toward the front of a data transmission interval to samples in an interval obtained by subtracting a predetermined length from a useful symbol interval starting from the symbol timing synchronization point, and inputs the result of addition to the FFT operation unit 240.

[0015] The FFT operation unit 230 performs FFT with respect to samples corresponding to the useful symbol interval output from the FFT window controller 120, and outputs a frequency domain signal. The frequency domain signal includes a scattered pilot signal containing information necessary for OFDM transmission in addition to general data. Scattered pilot signals which have been inserted into each symbol of the frequency domain signal at intervals of 12 samples are necessary for symbol timing synchronization.

[0016] The scattered pilot mode detector 250 decides the mode of a scattered pilot and compares the sums of powers of subcarriers corresponding to each scattered pilot mode with one another based on the feature that a pilot has greater power than usual data to detect a mode having greatest power among four modes.

[0017] The scattered pilot extractor 260 extracts scattered pilots which have been inserted into a symbol using a scattered pilot start mode detected by the scattered pilot mode detector 250.

[0018] The symbol timing offset estimator 270 estimates a symbol timing offset composed of an integer part and a fraction part using scattered pilots extracted by the scattered pilot extractor 260. The integer part is

applied to the FFT window controller 230 to perform fine symbol timing synchronization, and the fraction part is applied to the ADC 210 through the DPLL 280 to control the sampling clock signal of the ADC 210.

[0019] The DPLL 280 changes the frequency and phase of a sampling clock signal of the ADC 210 using the fraction part of the a symbol timing offset output from the symbol timing offset estimator 260.

[0020] FIG. 3 is a conceptual diagram of a method of setting an FFT window period in an FFT window controller according to the present invention. Referring to (a) of FIG 3, the FFT window controller 230 sets a symbol timing synchronization point in a data transmission interval composed of a guard interval  $T_g$  and a useful symbol interval  $T_u$  using a timing symbol offset estimated by the symbol timing offset estimator 270. Next, the FFT window controller 230 adds samples in a predetermined interval  $T_p$  extending from the symbol timing synchronization point toward the front of the data transmission interval to samples in an interval obtained by subtracting a predetermined length from the useful symbol interval  $T_u$  starting from the symbol timing synchronization point. Here, the predetermined interval  $T_p$  is set to be shorter than the guard interval  $T_g$  according to a channel environment.

[0021] Referring to (b) of FIG. 3, the samples in the predetermined interval  $T_p$  correspond to a guard interval obtained by copying a portion of the useful symbol interval  $T_u$  and thus are included in the symbol to be fast Fourier transformed. Accordingly, the single symbol is fast Fourier transformed, thereby preventing intersymbol interference (ISI) in the OFDM receiving system.

[0022] FIG. 4 is a conceptual diagram of a method of resetting an FFT window period in an FFT window controller and inputting reset information to an FFT operation unit. The FFT window controller 230 adds samples in a predetermined interval  $T_p$  extending from a symbol timing synchronization point set in a data transmission interval toward the front of the data transmission interval to samples in an interval obtained by subtracting a predetermined length from a useful symbol interval  $T_u$  starting from the symbol timing synchronization point and inputs the result of addition to the FFT operation unit 240. For example, when a data transmission mode is 2 K, the FFT window controller 230 inputs 2048 samples to the FFT operation unit 240 in parallel.

[0023] FIG. 5 is a flowchart of a symbol timing synchronization method in an OFDM receiving system according to the present invention. In step 510, an OFDM signal is received in units of symbols from an OFDM transmitting system.

[0024] Next, in step 520, scattered pilots which have been inserted into a symbol at intervals of predetermined samples are extracted from the received OFDM signal.

[0025] Next, a symbol timing offset is estimated using the extracted scattered pilots in step 530, and a symbol timing synchronization point is set in a data transmission

interval composed of a guard interval and a useful symbol interval in step 540.

[0026] Next, in step 550, samples in a predetermined interval extending from the symbol timing synchronization point toward the front of the data transmission interval are added to samples in an interval obtained by subtracting a predetermined length from a useful symbol interval starting from the symbol timing synchronization point, thereby resetting an FFT window period.

[0027] FIG. 6 is a graph of the performance of the present invention versus the performance of prior art in symbol timing synchronization. The graphs shows the square mean  $\text{SAMPLE}^2$  of a symbol timing offset remaining after symbol timing synchronization in a digital video broadcasting-terrestrial (DVB-T) system. A test was performed in a data transmission mode of 2 K, in a guard interval length mode of 1/16, and with an initial sample offset of 1000 samples, a sampling clock frequency offset of 200 ppm, an initial sampling clock phase offset of 200 ppm, and a loop bandwidth of 500 ppm in a PLL block. 20 channels according to the DVB-T test specifications are used as a multi-path. The present invention has a lower square mean  $\text{SAMPLE}^2$  of a remaining symbol timing offset than the prior art with respect to a Rayleigh channel and a Ricean channel.

[0028] FIG. 7 is a graph of symbol error rates (SERs) exhibited after symbol timing synchronization and channel equalization according to the present invention and prior art. A test was performed under the same conditions as in FIG. 6. For a channel-equalization algorithm, channel estimation and equalization are performed by performing linear interpolation using scattered pilots during a 4-symbol period. Referring to FIG. 7, the present invention has a lower SER than the prior art with respect to a Rayleigh channel.

[0029] As described above, according to the present invention, symbol timing synchronization and channel equalization can be accurately performed without having ISI even when a symbol start point is not correctly decided under a multi-channel (or a Rayleigh channel) or a pre-ghost channel environment.

[0030] The present invention is not restricted to the embodiments described above, and it will be understood by those skilled in the art that various changes in form and details may be made therein. In other words, the present invention can be applied to European type digital TV, wireless LANs according to IEEE 802.11a and other systems using OFDM.

[0031] The present invention can be embodied as a program which can be executed in a computer. The program can be read from a medium used in a computer and executed by the computer. The medium may be a storage medium such as a magnetic storage medium (for example, a ROM, a floppy disc, or a hard disc), an optical readable medium (for example, a CD-ROM or a DVD), or a carrier wave (for example, transmission through Internet). In addition, the program can be distributively stored in computer readable recording media

in computer systems connected through a network and can be executed.

## 5 Claims

1. An orthogonal frequency division multiplex (OFDM) receiving method of performing fast Fourier transform (FFT) with respect to a data transmission interval of a symbol unit composed of a guard interval and a useful symbol interval, the method comprising the steps of:

extracting scattered pilots, which are inserted into a symbol at intervals of predetermined samples, from a received OFDM signal; estimating a symbol timing offset using the extracted scattered pilots and setting a symbol timing synchronization point in the data transmission interval; and adding samples in a predetermined interval in the data transmission interval to samples in an interval obtained by subtracting a predetermined length from the useful symbol interval starting from the symbol timing synchronization point and performing FFT with respect to the result of addition.

2. The method of claim 1, wherein the predetermined interval in the data transmission interval extends from the symbol timing synchronization point toward the front of the data transmission interval.

3. The method of claim 1 or 2, wherein the predetermined interval in the data transmission interval is shorter than the guard interval.

4. The method of one of the claims 1 to 3, wherein the predetermined interval in the data transmission interval changes depending on a channel environment.

5. An orthogonal frequency division multiplex (OFDM) receiving system for performing fast Fourier transform (FFT) with respect to a data transmission interval of a symbol unit composed of a guard interval and a useful symbol interval, the system comprising:

an analog-to-digital converter for converting an OFDM signal into digital complex samples; FFT operation means for performing FFT with respect to the useful symbol interval; a scattered pilot extractor for extracting scattered pilots from samples output from the FFT operating means; a symbol timing offset estimator for estimating a symbol timing offset using scattered pilots ex-

tracted by the scattered pilot extractor; and  
an FFT window controller for setting a symbol  
timing synchronization point in a data transmis-  
sion interval received from the analog-to-digital  
converter, adding samples in a predetermined 5  
interval extending from the symbol timing syn-  
chronization point toward the front of the data  
transmission interval to samples in an interval  
obtained by subtracting a predetermined length  
from a useful symbol interval starting from the 10  
symbol timing synchronization point, and input-  
ting the result of addition to the FFT operation  
means.

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FIG. 1 (PRIOR ART)

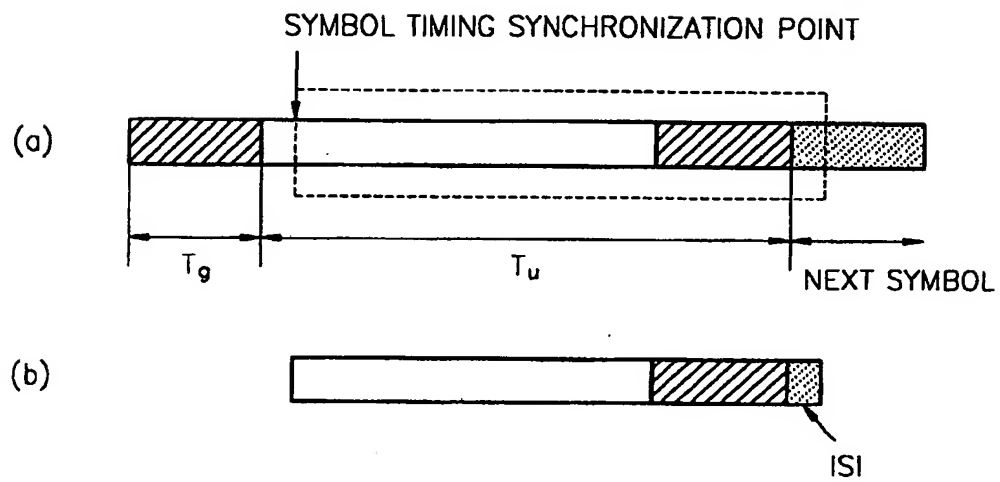


FIG. 2

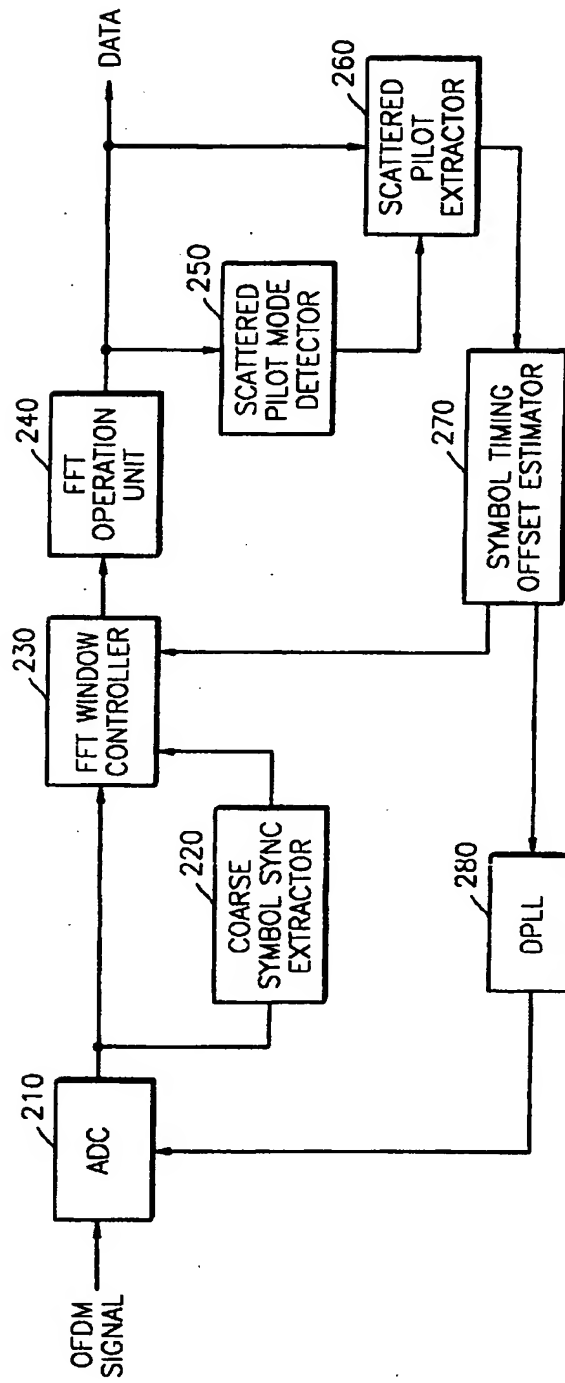


FIG. 3

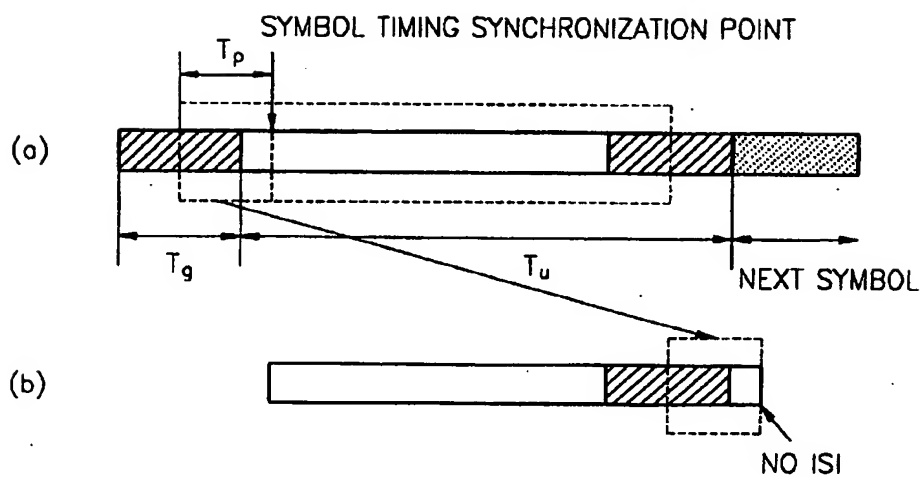


FIG. 4

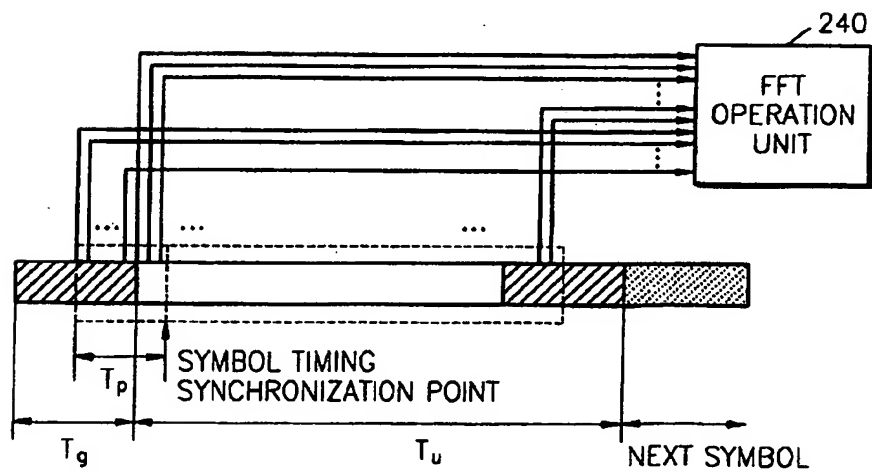




FIG. 5

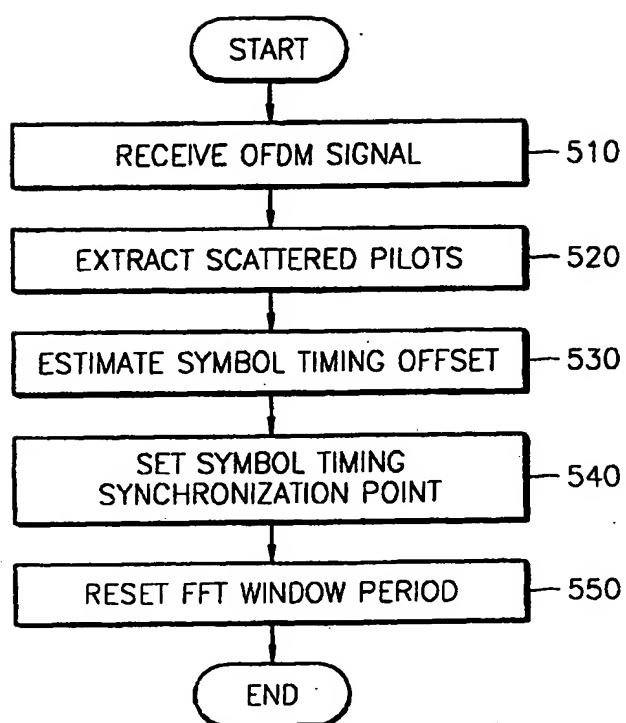


FIG. 6

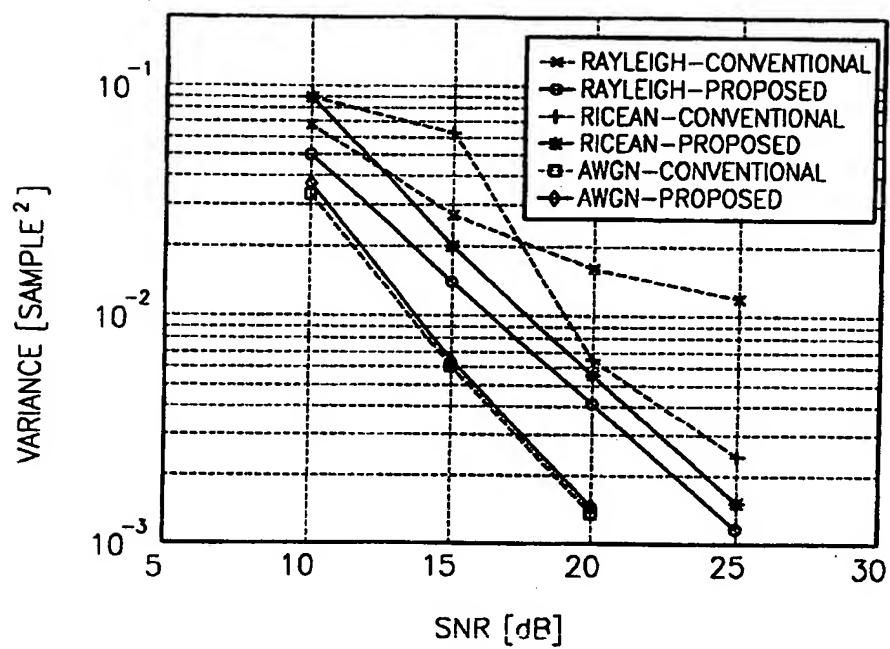
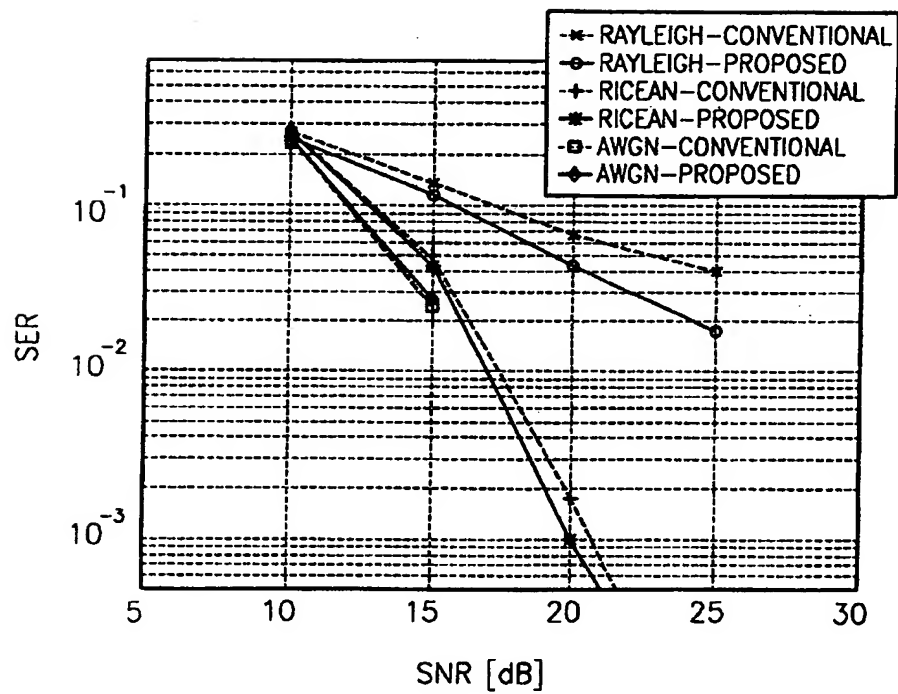


FIG. 7







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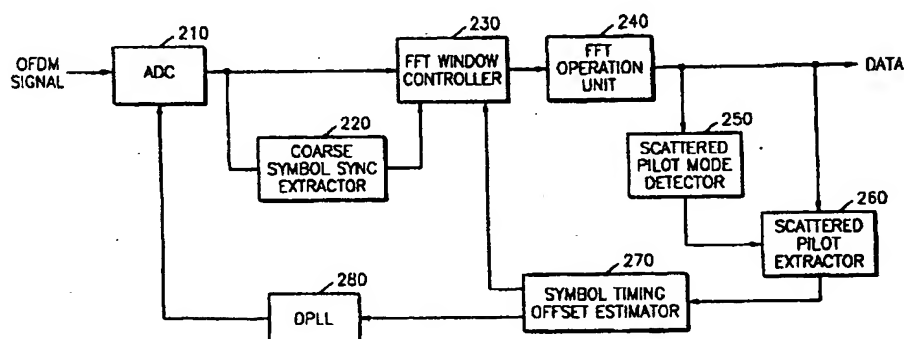
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(54) **OFDM receiving system for performing system timing synchronization using guard interval and method for the same**

(57) An orthogonal frequency division multiplex (OFDM) receiving system for changing a fast Fourier transform (FFT) window period using a guard interval, and a method for the same are provided. The method includes extracting scattered pilots, which are inserted into a symbol at intervals of predetermined samples, from a received OFDM signal, estimating a symbol timing offset using the extracted scattered pilots and setting a symbol timing synchronization point in the data transmission interval, and adding samples in a predeter-

mined interval in the data transmission interval to samples in an interval obtained by subtracting a predetermined length from the useful symbol interval starting from the symbol timing synchronization point and performing FFT with respect to the result of addition. Accordingly, symbol timing synchronization and channel equalization can be accurately performed without having ISI even when a symbol start point is not correctly decided under a multi-channel (or a Rayleigh channel) or a pre-ghost channel environment.

**FIG. 2**



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## EUROPEAN SEARCH REPORT

Application Number  
EP 01 13 0993

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
A	EP 1 063 824 A (MATSUSHITA ELECTRIC IND CO LTD) 27 December 2000 (2000-12-27) * column 1, line 32 - line 43 * * column 2, line 5 - line 42 * * column 15, line 29 - column 16, line 43 * * column 17, line 44 - column 18, line 31 * * column 26, line 10 - column 29, line 51 *	1-5	H04L27/26 H04L25/03
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A	KIM D K ET AL: "A NEW JOINT ALGORITHM OF SYMBOL TIMING RECOVERY AND SAMPLING CLOCK ADJUSTMENT FOR OFDM SYSTEMS", IEEE TRANSACTIONS ON CONSUMER ELECTRONICS, IEEE INC. NEW YORK, US, VOL. 44, NR. 3, PAGE(S) 1142-1148 XP000851632 ISSN: 0098-3063 * Section IV *	1-5	TECHNICAL FIELDS SEARCHED (Int.Cl.7) H04L
The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 11 April 2003	Examiner Palacián Lisa, M
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure P: intermediate document		T: theory or principle underlying the invention E: earlier patent document, but published on, or after the filing date O: document cited in the application L: document cited for other reasons &: member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 13 0993

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11-04-2003

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